

Electronic Patent Application Fee Transmittal

Application Number:	10534962
Filing Date:	16-May-2005
Title of Invention:	Circuit board, multi-layer wiring boards, method of producing circuit boards and method of producing multilayer wiring boards
First Named Inventor/Applicant Name:	Masayoshi Kondo
Filer:	Robert G. Weilacher/Susan Revell
Attorney Docket Number:	033036.088

Filed as Large Entity

U.S. National Stage under 35 USC 371 Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 1 month with \$0 paid	1251	1	120	120

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				120